

PATENT

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ASSIGNEE: INTEL CORPORATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Satesh KOTTAPALLI et al.

SERIAL NO. : 09/751,762

FILED : December 29, 2000

FOR : METHOD FOR CONVERTING PIPELINE STALLS
CAUSED BY INSTRUCTIONS WITH LONG LATENCY
MEMORY ACCESSES TO PIPELINE FLUSHES IN A
MULTITHREADED PROCESSOR WHERE THE
INSTRUCTIONS ARE RE-EXECUTED UPON
COMPLETION OF THE ACCESSES

GROUP ART UNIT : 2183

EXAMINER : Shane F. GERSTL

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M/S: AMENDMENTS
Commissioner for Patents
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Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to
the Patent and Trademark Office, Fax No. (703) 872-9306, on
May 4, 2005.

Pilar Rodriguez

RESPONSE

The following amendments and remarks below are respectfully submitted in response
to the Office Action dated January 4, 2005.

Listing of the Claims begin on page 2 of this response.

Remarks/ Arguments begin on page 6 of this response.